PATENT ABSTRACTS OF JAPAN

(11)Publication number:

11-150391

(43) Date of publication of application: 02.06.1999

(51)Int.CI.

H05K 9/00 H01L 25/00

(21)Application number : 09-317195

(71)Applicant: SONY CORP

(22)Date of filing:

18.11.1997

(72)Inventor: NAKAMURA TOSHIFUMI

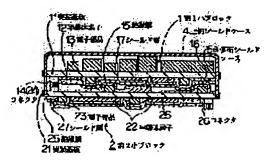
YONEYAMA KATSUHIRO

(54) ELECTRONIC CIRCUIT MODULE AND MANUFACTURE THEREOF

(57)Abstract:

PROBLEM TO BE SOLVED: To provide an electronic circuit module composed of electronic circuit small blocks which are laminated close to each other, wherein the blocks are surely insulated from each other, and the circuit module is lessened in thickness and weight at the same time.

SOLUTION: A thin film shielding layer 17 reflecting on the opposed rugged surface of a first small block 1 is interposed between the first small block 1 and a second small block 2 which are electrically connected together with connectors 14 and 24, whereby the small blocks 1 and 2 can be made to confront each other providing the smallest space between them. The shielding layer 17 is formed through a method wherein a thin insulating film



15 is formed on the first small block 1, then conductive paint is sprayed on the insulating film 15 to obtain a coating film, and the layer 17 is formed of the coating film. The shielding layer 17 is connected to the grounding electrode of the first small block 1 through an opening 16. The shielding layer 17 is insulated from the second small block 2 by an insulating film 25 formed on the surface of the second small block 2.

LEGAL STATUS

[Date of request for examination]

[Date of sending the examiner's decision of rejection]

[Kind of final disposal of application other than the examiner's decision of rejection or application converted registration]

[Date of final disposal for application]

[Patent number]

[Date of registration]

[Number of appeal against examiner's decision of rejection]

[Date of requesting appeal against examiner's decision of rejection]

[Date of extinction of right]

Copyright (C); 1998,2003 Japan Patent Office